

Low Noise, Wideband, High IP3

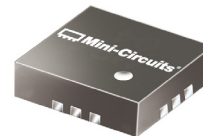
Monolithic Amplifier

PMA3-83LN+

50Ω 0.5 to 8.0 GHz

The Big Deal

- Flat gain over wideband
- Low noise figure, 1.3 dB
- High IP3, up to +35 dBm



CASE STYLE: DQ1225

Product Overview

The PMA3-83LN+ is a PHEMT based wideband, low noise MMIC amplifier with a unique combination of low noise, high IP3, and flat gain over wideband making it ideal for sensitive, high-dynamic-range receiver applications. This design operates on a single 5V or 6V supply, is well matched for 50Ω and comes in a tiny, low profile package (3 x 3 x 0.89mm), accommodating dense circuit board layouts.

Key Features

Feature	Advantages
Low noise, 1.3 dB at 2 GHz	Enables lower system noise figure performance.
High IP3 <ul style="list-style-type: none">• +35 dBm at 2 GHz• +28.5 dBm at 8 GHz	Combination of low noise and high IP3 makes this MMIC amplifier ideal for use in low noise receiver front end (RFE) as it gives the user advantages of sensitivity and two-tone IM performance at both ends of the dynamic range.
Low operating voltage, 5V/6V.	Achieves high IP3 using low voltage.
3 x 3mm 12-lead MCLP package	Tiny footprint saves space in dense layouts while providing low inductance, repeatable transitions, and excellent thermal contact to the PCB.
Wide bandwidth with flat gain <ul style="list-style-type: none">• ±0.9 dB over 0.5 to 7 GHz• ±1.5 dB over 0.5 to 8 GHz	Enables a single amplifier to be used in many wideband applications including defense, instrumentation and more.



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0.5-8.0 GHz

Product Features

- Low Noise figure, 1.3 dB at 2 GHz
- High IP3, 35 dBm typ. at 2 GHz
- High Pout, P1dB 20.7 dBm typ. at 2 GHz and 6V
- Excellent Gain flatness, ± 0.9 dB over 0.5 to 7 GHz and 6V



PMA3-83LN+

CASE STYLE: DQ1225

Typical Applications

- WiFi
- WLAN
- UMTS
- LTE
- WiMAX
- S-band Radar
- C-band Satcom

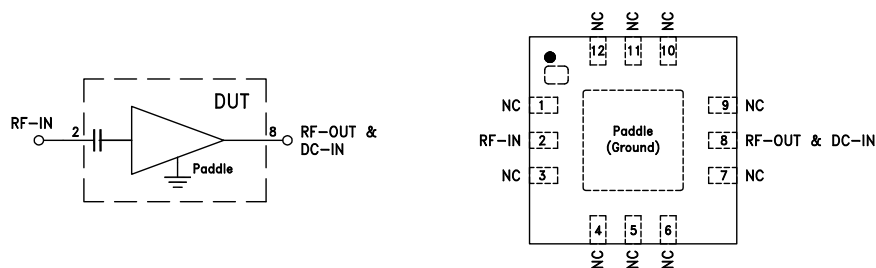
+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

General Description

The PMA3-83LN+ is a PHEMT based wideband, low noise MMIC amplifier with a unique combination of low noise, high IP3, and flat gain over wideband making it ideal for sensitive, high-dynamic-range receiver applications. This design operates on a single 5V or 6V supply, is well matched for 50Ω and comes in a tiny, low profile package ($3 \times 3 \times 0.89\text{mm}$), accommodating dense circuit board layouts.

simplified schematic & pad description



Function	Pad Number	Description (See Figure 1)
RF-IN	2	Connects to RF input and to ground via L1 (optional blocking capacitor of 100pF may be used)
RF-OUT & DC-IN	8	Connects to RF out via C3 and V_{DD} via L2
Ground	Paddle	Connects to ground
No Connection	1,3 to 7, 9 to 12	Not used internally. Connected to ground on test board (except 11 and 12)

Electrical Specifications¹ at 25°C and 5V, unless noted

Parameter	Condition (GHz)	V _{DD} =6.0			V _{DD} =5.0			Units
		Min.	Typ.	Max.	Min.	Typ.	Max.	
Frequency Range		0.5		8.0	0.5		8.0	GHz
Noise Figure	0.5	—	1.8	—	—	1.9	—	dB
	2.0	—	1.3	1.7	—	1.3	—	
	4.0	—	1.5	—	—	1.5	—	
	5.0	—	1.5	—	—	1.6	1.9	
	8.0	—	2.2	—	—	2.2	—	
Gain	0.5	—	21.8	—	—	21.0	—	dB
	2.0	19.9	22.1	24.3	—	21.3	—	
	4.0	—	21.5	—	—	20.8	—	
	5.0	—	21.2	—	18.7	20.5	—	
	8.0	—	19.2	—	—	18.7	—	
Input Return Loss	0.5		14.2			13.1		dB
	2.0		16.0			16.0		
	4.0		13.0			12.2		
	5.0		12.5			12.4		
	8.0		6.3			6.3		
Output Return Loss	0.5		12.9			13.7		dB
	2.0		13.0			14.3		
	4.0		28.8			27.5		
	5.0		20.3			18.4		
	8.0		12.9			12.6		
Output Power at 1dB Compression ²	0.5		18.6			16.3		dBm
	2.0		20.7			19.1		
	4.0		19.6			17.6		
	5.0		20.3		17.2	18.9		
	8.0		18.0			17.3		
Output IP3	0.5		34.2			29.7		dBm
	2.0		35.2			30.0		
	4.0		34.0			29.6		
	5.0		34.0		24.0	29.7		
	8.0		28.5			26.2		
Device Operating Voltage (V _{DD})			6.0	—		5.0		V
Device Operating Current (I _{DD})			77	94		60		mA
Device Current Variation vs. Temperature ³			-152	—		-109		μA/°C
Device Current Variation vs. Voltage			0.016			0.016		mA/mV
Thermal Resistance, junction-to-ground lead			47			47		°C/W

1. Measured on Mini-Circuits Characterization test board TB-830A+. See Characterization Test Circuit (Fig. 1)

2. Current increases at P1dB to 109 mA typ. at +6V VDD and 88mA typ. at +5V VDD

3. (Current at 85°C - Current at -45°C)/130

Absolute Maximum Ratings⁴

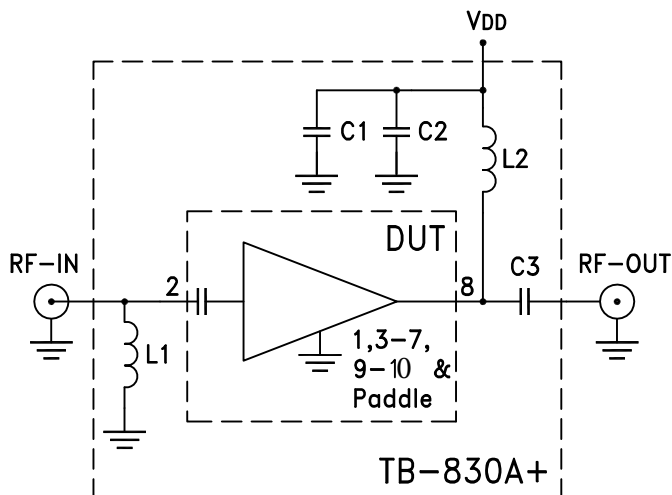
Parameter	Ratings
Operating Temperature (ground lead)	-40°C to 85°C
Storage Temperature	-65°C to 150°C
Junction Temperature	150
Total Power Dissipation	0.95 W
Input Power (CW), Vd=5.6V ⁵	+19 dBm (5 minutes max.) +16 dBm (continuous)
DC Voltage	7 V

4. Permanent damage may occur if any of these limits are exceeded.
Electrical maximum ratings are not intended for continuous normal operation.

5. Measured on Mini-Circuits test board, TB-830A+



Recommended Application and Characterization Test Circuit



Component	Value	Size
C1	0.01 μ F	0402
C2	10pF	0402
C3	100pF	0402
L1	18nH	0402
L2	39nH	0402

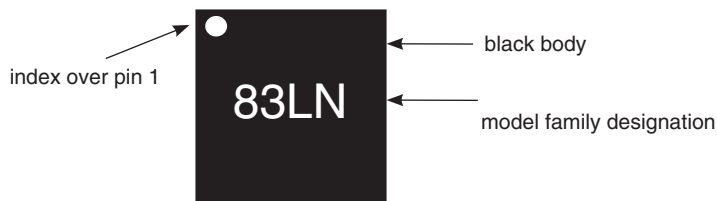
Fig 1. Application and Characterization circuit

Note: This block diagram is used for characterization. (DUT soldered on Mini-Circuits Characterization test board TB-830A+) Gain, Return loss, Output power at 1dB compression (P1 dB) , output IP3 (OIP3) and noise figure measured using Agilent's N5242A PNA-X microwave network analyzer.

Conditions:

1. Gain and Return loss: Pin= -25dBm
2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, 0 dBm/tone at output.

Product Marking



Additional Detailed Technical Information	
<i>additional information is available on our dash board. To access this information click here</i>	
Performance Data	Data Table
	Swept Graphs
	S-Parameter (S2P Files) Data Set (.zip file)
Case Style	DQ1225 <i>Plastic package, exposed paddle, lead finish: tin silver over nickel</i>
Tape & Reel Standard quantities available on reel	F66 <i>7" reels with 20, 50, 100, 200, 500, 1K or 2K devices</i>
Suggested Layout for PCB Design	PL-456
Evaluation Board	TB-830A+
Environmental Ratings	ENV08T1

ESD Rating

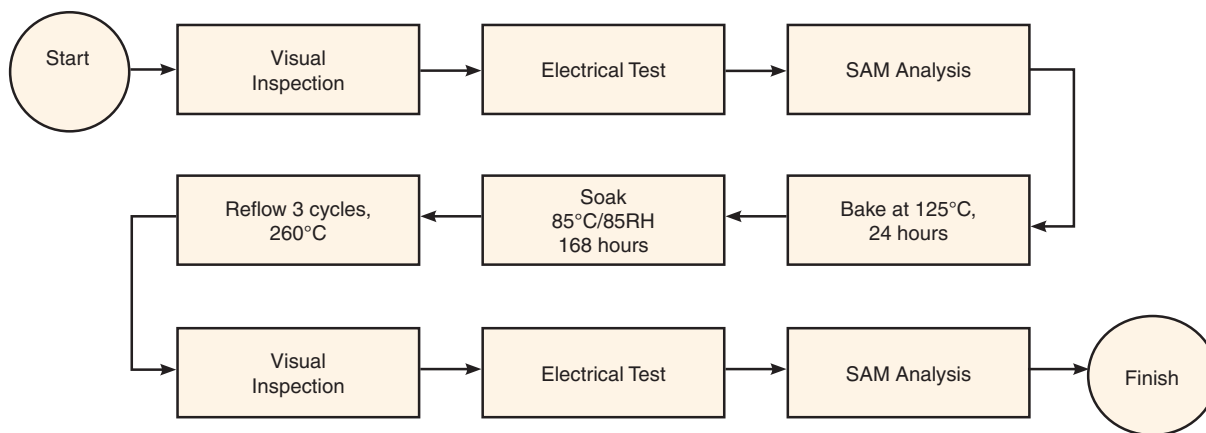
Human Body Model (HBM): Class 1A (250 to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (pass 50V) in accordance with ANSI/ESD STM5.2-1999

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

MSL Test Flow Chart



Additional Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

